



## ABSTRACT OF THE DISCLOSURE

A method of decoding a bit stream having an embedded clock, where the clock reference data is recovered from the bit stream. The clock reference data is used to create an adjusting value control a local clock frequency. The adjustment calculated such that the local clock frequency and the local clock value match the frequency and values in the clock reference data. The adjustment value is input to pulse generator to form a pulse train, which is used to generate the input to an adjustable oscillator.

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